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455610-2550IN THE CLAIMS:

The status of the claims is noted below.

1. (Currently Amended) A signal processing system capable of compensating for the channel response characteristics of an input waveform, comprising:

input means for inputting input specifications for specifying the design of a filter,

including:

channel response characteristics defining the response characteristics of a channel used to acquire said input waveform; and

user specifications for specifying a desired frequency response and a

degree of compliance to the desired frequency response;

a filter builder for generating coefficients for said filter and outputting final performance specifications, having:

a compensation filter generator for generating coefficients corresponding to a compensation response on the basis of the inverse of the channel response characteristics; and

a response filter generator for generating coefficients corresponding to a combination of an ideal response and a noise reduction response on the basis of the user specifications; and

said filter for filtering said input waveform and outputting an overall response waveform having said desired frequency response, comprising:

a filter coefficient cache for storing the coefficients generated by said filter builder;

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a compensation filter portion for filtering said input waveform using the coefficients stored in said filter coefficient cache corresponding to said compensation response; and

a response filter portion having a response filter stage and a noise reduction stage for filtering the compensated waveform output from said compensation filter portion and outputting said overall response waveform; said response filter portion filtering using the coefficients stored in said filter coefficient cache corresponding to said combination of said ideal response and said noise reduction response.

2. (Original) The signal processing system according to claim 1, wherein said filter is implemented as an infinite impulse response (IIR) filter.

3. (Original) The signal processing system according to claim 1, wherein said filter is implemented as a finite impulse response (FIR) filter.

4. (Original) The signal processing system according to claim 1, wherein said channel response characteristics are predetermined based on a reference signal and the reference signal as acquired by said channel.

5. (Original) The signal processing system according to claim 1, wherein said user specifications comprise a bandwidth, a response optimization, a compensation compliance, and a filter implementation type.

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6. (Original) The signal processing system according to claim 5, wherein said response optimization is a pulse response optimization implemented using a Besselworth filter.

7. (Original) The signal processing system according to claim 5, wherein said response optimization is a noise performance optimization implemented using a Butterworth filter.

8. (Original) The signal processing system according to claim 5, wherein said response optimization is a flatness optimization implemented using a Butterworth filter.

a1 9. (Original) The signal processing system according to claim 5, wherein said filter implementation type is finite impulse response (FIR) or infinite impulse response (IIR).

10. (Original) The signal processing system according to claim 1, wherein said user specifications default to predetermined values.

11. (Original) A signal processing element for filtering an input digital waveform, comprising:

a filter builder for generating filter coefficients on the basis of a channel frequency response and a user response characteristics; said channel frequency response being determined on the basis of a response input and a correction input;

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an infinite impulse response (IIR) filter having an IIR input for said input digital waveform and an IIR coefficient input connected to said filter builder; said IIR filter producing an IIR filtered waveform from the input digital waveform on the basis of the filter coefficients generated by said filter builder;

a finite impulse response (FIR) filter having an FIR input for said input digital waveform and a FIR coefficient input connected to said filter builder; said FIR filter producing a FIR filtered waveform from the input digital waveform on the basis of the filter coefficients generated by said filter builder; and

an output selector switch for selecting either said IIR filtered waveform or said FIR filtered waveform for output.

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12. (Original) The signal processing element according to claim 11, wherein said filter builder detects changes in the sampling rate of said input digital waveform that require the filter coefficients to be generated.

13. (Original) The signal processing element according to claim 11, wherein said filter builder generates filter coefficients for said FIR filter or said IIR filter on the basis of said output selector switch.

14. (Original) The signal processing element according to claim 11, wherein said filter builder has channel, compensation, shaper, and noise reduction outputs for evaluating the performance of the filtering.

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15. (Original) The signal processing element according to claim 11, wherein said response input is a known input response and said correction input is a measured input response as acquired by an input channel.

16. (Original) The signal processing element according to claim 11, wherein said user response characteristics are used to generate filter coefficients corresponding to an arbitrary response portion of the filter.

17. (Original) The signal processing element according to claim 11, wherein said user response characteristics comprise a bandwidth, a response optimization, a compensation compliance, and a filter implementation type.

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18. (Original) The signal processing element according to claim 17, wherein said response optimization is a pulse response optimization implemented using a Besselworth filter.

19. (Original) The signal processing element according to claim 17, wherein said response optimization is a noise performance optimization implemented using a Butterworth filter.

20. (Original) The signal processing element according to claim 17, wherein said response optimization is a flatness optimization implemented using a Butterworth filter.

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21. (Original) The signal processing element according to claim 17, wherein said filter implementation type is FIR or IIR.
22. (Original) The signal processing element according to claim 11, wherein said user response characteristics default to predetermined values.
23. (Original) A method of filtering an input digital waveform to compensate for the response characteristics of an acquisition channel, comprising the steps of:
- generating a compensation portion of a filter on the basis of an input channel response, using the steps of:
 - pre-warping said input channel response;
 - designing an analog filter emulating the pre-warped input channel response by making an initial filter guess and iterating the coefficients of said initial filter guess to minimize a mean-squared error;
 - inverting said analog filter; and
 - digitizing the inverted analog filter to produce said compensation portion of said filter using a bilinear transformation; and
 - filtering said input digital waveform using said compensation portion of said filter.
24. (Original) The method according to claim 23, further comprising the step of generating an arbitrary response portion of said filter on the basis of an input user specifications,

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wherein said input digital waveform is filtered using said arbitrary response portion of said filter, thereby producing a filtered digital waveform having the desired response characteristics.

25. (Original) The method according to claim 24, wherein said input user specifications comprise a bandwidth, a response optimization, a compensation compliance, and a filter implementation type.

26. (Original) The method according to claim 24, wherein said arbitrary response portion of said filter comprises a shaper and a noise reducer.

27. (Original) The method according to claim 24, wherein said input user specifications default to predetermined values.

28. (Original) The method according to claim 23, wherein said filter is implemented as an infinite impulse response (IIR) filter.

29. (Original) The method according to claim 23, wherein said filter is implemented as a finite impulse response (FIR) filter.

30. (Original) The signal processing system according to claim 23, wherein said input channel response is predetermined based on a reference signal and the reference signal as acquired by said channel.

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31. (Canceled)

32. (Canceled)

33. (Canceled)

a' 34. (Currently Amended) The method according to claim 23, wherein said filter

implementation type is FIR or IIR.

35. (Currently Amended) The method according to claim 23, wherein the coefficients
of said initial filter guess are iterated until said mean-squared error is less than a compensation
compliance specified in said an input user specifications.